	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	121853	map$4 with(space or area or region or block or range or page)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:13
2	BRS	L2	28440	1 with(memory or storage or ram or rom or nvram or nvrom)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:13
3	BRS	L3	264199	(processor or cpu or microprocessor or microcontroller)with(space or area or region or block or range or page)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:14
4	BRS	L4	38112	3 with(internal or inside or within)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:14
5	BRS	L5	1153	2 same 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:15
6	BRS	L6	4690	trap$4 with(code or kernel or opcode or instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:16

	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L7	0	5 same 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:16
8	BRS	L8	59	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:16
9	BRS	L9	687	trap adj4 handler	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:16
10	BRS	L10	0	5 same 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:16
11	BRS	L11	20	5 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:16
12	BRS	L12	20	11 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:16

	Type	L #	Hits	Search Text	DBs	Time Stamp
13	BRS	L13	858	harvard adj4 architecture	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:17
14	BRS	L14	40	5 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/29 16:17

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S6      3510     TRAP????(16N)(CODE OR KERNEL OR OPCODE OR INSTRUCTION)
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S8      125      TRAP(4W)HANDLER
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S11     0        S8 AND S5
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